| Substitute Form PTO-1449 (Modified) | U.S. Department of Commerce Patent and Trademark Office | Attorney's Docket No. 10559-449002 | Application No. 10/829,075 | |
|---|--|------------------------------------|----------------------------|--|
| Information Disclosure Statement by Applicant | | Applicant Daniel Yellin et al. | | |
| | heets if necessary) | Filing Date April 21, 2004 | Group Art Unit 2133 | |

| | U.S. Patent Documents | | | | | | |
|---------------------|-----------------------|--------------------|---------------------|--------------------|-------|----------|---------------------------------------|
| Examiner Initial | Desig. ID | Document Number | Publication Date | Patentee | Class | Subclass | Filing Date If Appropriate |
| · /JT | AA | 2002/0194567 | 12/2002 | Yellin et al. | | | |
| /JT/ | AB | 5,983,384 | 11/1999 | Ross | | | |
| /JT/ | AC | 6,253,185 | 06/2001 | Arean et al. | | | |
| /JT/ | AD | 6,393,072 | 05/2002 | Ross et al. | | | |
| /JT/ | AE | 6,516,437 | 02/2003 | Van Stralen et al. | | | |
| /JT/ | AF | 6,516,444 | 02/2003 | Maru | | | |
| /JT/ | AG | 6,725,409 | 04/2004 | Wolf | | | |
| | AH | | | | | | |
| | AI | | | | | | , , , , , , , , , , , , , , , , , , , |
| | AJ | | | | | | |

| Foreign Patent Documents or Published Foreign Patent Applications | | | | | | | | |
|---|--------|----------|-------------|---------------|-------|----------|-------------|----|
| Examiner | Desig. | Document | Publication | Country or | Class | Subclass | Translation | |
| Initial | 1 - 1 | Number | Date | Patent Office | | | Yes | No |
| | AK | | | | | | | |
| | AL | | | | | | | |
| | AM | | | | | | | |
| | AN | | | | | | | |
| <u> </u> | AO | 10 | | | | | | |

| | Other Documents (include Author, Title, Date, and Place of Publication) | | | | |
|--------------------|---|---|--|--|--|
| Examiner . Initial | Desig. ID | Document | | | |
| /JT/ | AP | Bahl, L.R., et al., "Optimal Decoding of Linear Codes for Minimizing Symbol Error Rate", IEEE Trans. Inform. Theory, pp. 248-287, March 1974. | | | |
| /JT/ | AQ | Forney, G.D., "The Viterbi Algorithm", Proc. IEEE, 61(3):268-278, March 1973. | | | |
| /JT/ | AR | Smit, G., et al., "Mapping the SISO module of the Turbo decoder to a FPFA", Proc. of Second International Symposium on Mobile Multimedia Systems & Applications (MMSA2000), pp. 165-172, November 2000. | | | |
| /JT/ | AS | Yeo, E., et al., "VLSI Architecture for Iterative Decoders in Magnetic Recording Channels", IEEE Transactions on Magnetics, 37(2):748-755, March 2000. | | | |
| | AT | | | | |

| Examiner Signature /Joseph Torres/ | Date Considered 4/25/2007 | | | |
|--|---------------------------|--|--|--|
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | |